



Phase Matrix, Inc.

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To: Users of the Models 585C and 588C
Subject: Memory clearing for security

To All,

The Model 585C/588C contains an 8K x 8 bit wide RAM that is used for temporary storage. The data is lost when the power is removed. The unit also contains a 64K x 8 bit PROM that contains the program for the operation of the counter and can not be modified by the user. The counter also contains a 8K x 8 bit EEPROM that is used for the calibration of the unit and contains the storage of option settings. This can only be modified by the user if the cover is removed and a jumper is placed across the calibration pins located on the CPU board. Any attempt to modify this part could result in a unit that no longer functions. The unit also has 10 storage registers that can be used to store various setup conditions with register 0 being a special case. Any setup stored in register 0 becomes the default setup. All 10 registers can be cleared using special function 72 then pressing the data clear button. This action returns the unit to factory defaults.

If you have any additional questions regarding the 585C/ 588C I can be reached at 408-954 6406.

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